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(54) Attaching an electronic circuit to a substrate

(57) Integrated circuits such as transponders (10) are attached to a substrate (28) such as cardboard packaging material by adhering an integrated circuit to the substrate, applying a patch antenna (18) to the substrate and the integrated circuit, and applying a sealing layer (40) over the integrated circuit and at least a portion of the antenna. In variations of the method, an in-

dentation (34) may be formed in the substrate into which the integrated circuit is placed. The patch antenna may be screen printed onto the substrate and the integrated circuit, using conductive ink, or may be a metallic foil which is applied to the substrate and the integrated circuit. Where the integrated circuit is a transponder, it can be programmed with an identification code, either before or after being attached to the substrate.

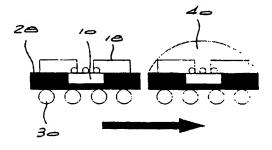


Figure 6



EUROPEAN SEARCH REPORT

Application Number EP 94 30 1777

DOCUMENTS CONSIDERED TO BE RELEVANT Citation of document with indication, where appropriate, Relevant			t CLASSIFICATION OF THE	
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A	US-A-4 546 357 (LAUGHON T.C. ET AL.) * claim 1 *		2	1,02227,00
A	PATENT ABSTRACTS OF vol. 17, no. 32 (E-1 & JP-A-04 252523 (M) CORP.), 8 September * abstract *	1309), 21 January 1993 ITSUBISHI ELECTRIC	3	
Α	MICROWAVE JOURNAL, vol. 19, no. 10, Oc pages 47-49, XP00200 GREISER,J.W.: "COP ANTENNA" * column 2 * * figure 1B *	07867	2	
A	DE INGENIEUR, vol. 84, no. 24, 16 pages 74-80, XP0020 FLACHENECKER,G. ET ANTENNAS" * page 76; figure 4	07868 AL.: "ACTIVE RECEIVIN	G 1	TECHNICAL FIELDS SEARCHED (Int.Cl.5) G06K H01Q
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A	GB-A-2 100 669 (GAO ORGANISATION MBH) * abstract *	GES FUR AUTOMATION UN	12,13	
		-/		
	The present search report has b	seen drawn up for all claims	_	
	Place of search	Date of completion of the search		Examiner
X:p Y:p d A:te O:n P:ie	THE HAGUE	9 July 1996		Herskovic, M
CATEGORY OF CITED DOCUMENTS X: particularly relevant if taken alone Y: particularly relevant if combined with another document of the same category A: technological background O: non-written disclosure		E : earliér paient after the fill in other D : document cit L : document cit	document, bug date ed in the applied for other re	it published on, or ication



CL	AIMS INCURRING FEES
The present	European patent application comprised at the time of filing more than ten claims.
	All claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for all claims.
	Only part of the claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims and for those claims for which claims fees have been paid.
	namely claims:
	No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims.
LA	CK OF UNITY OF INVENTION
	Division considers that the present European patent application does not comply with the requirement of unity of
	nd relates to several inventions or groups of inventions,
namely:	
}	SEE SHEET -B-
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×	All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.
	Only part of the further search fees have been paid within the fixed time limit. The present European search
	report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid,
	namely claims:
	None of the further search fees has been paid within the fixed time limit. The present European search report
	has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims,
	namely claims:



EUROPEAN SEARCH REPORT

Application Number EP 94 30 1777

DOCUMENTS CONSIDERED TO BE RELEVANT Category Citation of document with indication, where appropriate, Relevant					
Category	of relevant pa	ndication, where appropriate, assuges	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.CL5)	
A	GB-A-2 081 974 (GAC AUTOMATION UND ORGA * claim 1 *	GESELLSCHAFT FUR NISATION MBH)	14		
A	INTERNATIONAL ELECT TECHNOLOGY SYMPOSID 26 - 28 April 1989 pages 101-104, XPOO WATAHIKI,S. ET AL.: TECHNOLOGY OF SUPER * figure 1 *	, NARA,JAPAN, 10077902 "NEW PACKAGING	16,18		
A	DE-A-41 05 869 (SCH * figure 2 *	NEIDER,E.)	17		
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A	EP-A-0 436 497 (TRE * claims 5,6 *	END PLASTICS, INC.)	22		
A	US-A-4 004 133 (HANNAN,W. * claim 1 * GB-A-2 208 058 (ELECTROMALTD) * claim 15 *	NAN,W.J. ET AL.)	24		
A		CTROMATIC (PROPRIETARY)	24		
A	US-A-3 755 803 (COL * column 2, line 34	E,P.H. ET AL.) line 37 *	28		
A	FR-A-2 336 741 (KREFT,H. * claim 11 *	FT,H.D.)	29		
A	EP-A-0 268 089 (ETA D'EBAUCHES) * column 4, line 21	·	4		
		-/			
	The present search report has b	een drawn up for all claims	1		
~	Place of search	Date of completion of the search		Examiner	
	THE HAGUE	9 July 1996	Her	skovic, M	
X : par Y : par doc A : tecl O : nor	CATEGORY OF CITED DOCUME ticularly relevant if taken alone ticularly relevant if combined with an unent of the same category nological background written disclosure rmediate document	E : earlier patent do after the filing d D : document cited f L : document cited f	cument, but publicate in the application or other reasons	ished on, or	

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European Patent Office

EP 94301777-B-

LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application odes not comply with the requirement of unity of invention and relates to several inventions or groups of inventions.

Subject 1: Claims 1-3, 27-32
Method of attaching an electronic circuit or a packaging component comprising an integrated circuit to a substrate and applying a conductive element (layer of conductive ink comprising carbon or metallic particles) to define an antenna contacting at least 2 electrical terminals of the electronic circuit and the corresponding identification tag.

Subject 2: Claims 1, 4-11, 27-32 Method of applying a metallic foil to contacting the electrical terminals of an integrated circuit or a packaging component comprising an integrated circuit and securing together on a substrate whether with a conductive adhesive or applying solder and the corresponding identification tag.

Subject 3: Claims 1, 12-17, 27-32
Method of attaching an electronic circuit or a packaging component comprising an integrated cicuit on a substrate by forming a cavity in the substrate made by punching, moulding, application of a heated tool, to receiving the electronic circuit; by heating the electronic circuit and pressing it in the substrate and the corresponding identification tag.

Subject 4: Claims 1, 18-20, 26, 27-32
Packaging component comprising an integrated circuit and method of applying a protective coating or enclosure over the integrated circuit and over a portion of the antenna, said enclosure comprising a layer of a settable sealant or a label and the corresponding identification tag.

Subject 5: Claims 1, 21-25, 27-32 ... Packaging component comprising an integrated circuit and method of attaching to a substrate the integrated circuit which is programmable with an idendity code and the corresponding identification tag.



EUROPEAN SEARCH REPORT

Application Number EP 94 30 1777

Category	Citation of document with ind		Relevant	CLASSIFICATION OF THE
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A	EP-A-0 246 744 (TOSH * claims 1,2 *	IBA K.K.)	10	
A	* claim 1 *	UMBERGER INDUSTRIES)	5	
				TECHNICAL FIELDS SEARCHED (Int.Cl.5)
			_	
	The present search report has been drawn up for all claims			
	Place of search	Date of completion of the search		Examiner
	THE HAGUE	9 July 1996	Hen	rskovic, M
	CATEGORY OF CITED DOCUMEN irticularly relevant if taken alone urticularly relevant if combined with ano	iple underlying th focument, but put date d in the applicatio	olished on, or	